## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: Y. KANNO et al

Serial No. 10/325,920 Filed: December 23, 2002

Group Art:

2818

Examiner:

G. Auduong

For:

SEMICONDUCTOR INTEGRATED CIRCUIT

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

June 27, 2003

Sir:

In accordance with the duty of disclosure, the Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 10/325,920.

The Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,

Daniel J/Stanger

Registration No. 32,846 Attorney for Applicant(s)

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Telephone: (703) 684-1120 Facsimile: (703) 684-1157

Date: June 27, 2003

FORM PTO-1449 (REV. 7-80)				MENT OF COMMERC TRADEMARK OFFIC	E ATTY, DOCKET NO.   SERIAL NO.   E ASA-994-03						
LIST OF DOCUMENTS CITED BY					APPLICANT Y. KANNO et al						
(Use several sheets if necessary					GROUP 2818	GROUP					
				U.S. PATEN	06/27/03 T DOCUMENTS	12010		-			
* EXAMINER DOCUMENT I		DATE	NAME		CLASS	SUBCLASS	FILING DATE (If Appropriate				
	AA	4,803,664	02/07/89	Itoh			(11111111111111111111111111111111111111				
		4,879,682	11/07/89	Engles							
	-	3,593,037	03/1970	Hoff, Jr.							
	AD	3,706,079	09/1971	Vadasz et al							
	AE	4,893,278	01/1990	Ito				-			
	AF	6,016,268	01/2000	Worley		-					
	AG	6,219,277	04/2001	Devin et al			-				
	AH 3,706,891 12/1972 Do:			Donofrio et a							
	Al	4,084,108	04/1978	Fujimoto							
	AJ 4,945,393 07/1990 B			Beltram et al							
	AK 5,396,452 03/1995 Wahlstrom										
		-		FOREIGN PAT	ENT DOCUMENTS						
		DOCUMENT	DATE	COUNTRY		CLASS	SUBCLASS	TRANS YES	NO NO		
	AL	11-134866 05/21/99		Japan (with English Abstract)							
	AM										
	AN										
	АО										
	AP										
			OTHER DOCU	MENTS (Including A	uthor, Title, Date, Pertinent I	Pages, etc.)					
	ΔR	K. Ito	, "Very La	rge Scale Inte	gration Memory", Ba	aifukan Co	o., Ltd.,				
AR			pp. 12-17.								
	AS										
	АТ										
EXAMINER					DATE CONSIDERED						
* EXAMINEI			ed, whether or not citati	on is in conformance with MPE	P 609; Draw line through citation if not in o	conformance and no	considered. Include	e copy of th	nis form		

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERC PATENT AND TRADEMARK OFFIC		E ATTY. DOCKET NO. E ASA-994-03	SERIAL NO.							
				APPLICANT								
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U.S. PATENT DOCUMENTS												
* EXAMINER INITIAL		DOCUMENT	DATE		NAME	CL	ASS	SUBCLASS	FILING (If Appr			
	AA	5,656,528	08/1997	Wahlstrom								
	AB	6,519,195	02/2003	Kanno et al								
	AC											
	AD											
	AE											
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EXAMINER				DATE CONSIDERED								
* EXAMINER	* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form next communication to applicant.											